REZULTATE LIVRATE 2008

1. Parser si compilator pentru descrieri HDL.
2. Algoritm de codificare eficientă a qubiților.
3. Algoritmi şi dispozitive pentru îmbunătățirea performanței operatiilor de virgulă flotantă

ARTICOLE PUBLICATE 2008

Oana Boncalo, Mihai Udrescu, Lucian Prodan, Mircea Vladutiu, Alexandru Amaricai **Error-Model Driven Analysis of Quantum Circuit's Reliability** Proceedings 8th IEEE Conference on Nanotechnology, August 18-21, 2008, Arlington, USA, ISBN 978-1-4244-2104-6 (cotat ISI Proceedings)

Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, Mihai Udrescu, Oana **Boncalo   Floating Point Multiplication Rounding Schemes for Interval Arithmetic** Proceedings 19th IEEE Conference on Application Specific Systems, Architectures and Processors (ASAP), July 2-4, 2008, Leuven, Belgium, pp. 19-24, ISBN 978-1-4244-1898-5 (cotat ISI Proceedings)

Lucian Prodan, Mihai Udrescu, Mircea Vladutiu **Fault-Tolerant Memory Design and Partitioning Issues in Embryonics** Evolvable Systems: From Biology to Hardware. LNCS5216, 8th International Conference ICES 2008, pp. 372-381(cotat ISI Proceedings)

Virgil Petcu, Alexandru Amaricai, Mircea Vladutiu **A Dual-Threaded Architecture for Interval Arithmetic Coprocessor with Shared Floating Point Units** Proceedings 11th IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems (DDECS 2008), Bratislava, Slovakia, April 16-18 2008, pp. 146-149, ISBN 978-1-4244-2276-0. (cotat ISI Proceedings)

Versavia Ancusa, Razvan Bogdan, Mircea Vladutiu **Discussing Redundancy Issues in Intelligent Agent-Based Non-traditional Grids** Proceedings of the 12th International Conference on Knowledge-Based and Inteligent Information and Engineering, September 3-5, 2008, LNCS, ISSN 0301-9743, pp 297-305 (cotat ISI Proceedings)

Cristian Ruican, Mihai Udrescu, Lucian Prodan, Mircea Vladutiu **Software Architecture for Quantum Circuit Synthesis ICAISC "Artificial Intelligence and Soft Computing**", Zakopane, Poland, June 2008, published in: Computational Intelligence: Methods and Applications, ISBN 978-83-60434-50-5, pp. 562-573

Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, Mihai Udrescu, Oana Boncalo **Floating Point Divide-Add Fused for Newton's Interval Method** Proceedings of the Euromicro Work In Progress Session held in Conjunction with Euromicro SEAA 2008 and Euromicro DSD 2008, September 3-5, Parma, Italy, ISBN 978-3-902457-20-3

Mihai Udrescu, Oana Boncalo,Lucian Prodan, Mircea Vladutiu, Alexandru Amaricai **Towards a Hybrid Methodology for Reliability Assessment of Quantum Circuits** Proceedings of the Euromicro

Work In Progress Session held in Conjunction with Euromicro SEAA 2008 and Euromicro DSD 2008, September 3-5, Parma, Italy, ISBN 978-3-902457-20-3

Versavia Ancusa, Razvan Bogdan, Mircea Vladutiu **Redundancy at Link Level for Non-traditional Grids Implemented with Intelligent Agents** Proceedings 4th International Conference on Networked Computing and Advance Information Management, September 2-4, 2008

Alexandru Amaricai, Mircea Vladutiu, Lucian Prodan, Mihai Udrescu, Oana Boncalo **Radix-4 Modified Booth Multiplication Algorithm** Proceedings 8th International Conference on Technical Informatics (CONTI), June 5-6, 2008, Timisoara pp 61-64, ISSN 1844-539-X